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Please select your course and name from the drop-down menu. If your course or name are incorrect or missing, contact the Curriculum and Assessment Administrative Assistant, 541-506-6037 or [swade@cgcc.edu](mailto:swade@cgcc.edu).

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EET 251- Digital Electronics 1: Programmable Logic Devices- Chris Spengler- Part A- Fall 2024

**\* Part A: Your Plan DIRECTIONS 1. Choose three of your course outcomes to assess and report on this term (these will also be used in your Student Course Evaluation survey): Outcome #1**

Recognize the differences between analog and digital systems and the advantages of digital.

**\* Outcome #2**

Read, express, and convert between decimal, binary, 2's complement, hex, BCD, Gray's, or octal number system.

**\* Outcome #3**

Determine the behavior of basic logic gates (AND, OR, NOT, NAND, NOR, XOR, and XNOR) in a circuit.

**Have you completed an assessment for this course prior to this term?**

Yes

**If yes, are you assessing different outcomes?**

No

**Comments:**

(No response)

**2. To which degree(s) or certificate(s) does your course map? Degree, Certificate, & Program Outcomes**

Associate of Applied Science: Electro-Mechanical Technology

**\* Method of Assessment 3. What methods will be used to assess individual student understanding of each of these outcomes? (Please be specific.) Outcome #1: Method to assess student understanding**

Worksheets, lab practical exercises, quizzes, and exams.

**\* Outcome #2: Method to assess student understanding**

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Worksheets, lab practical exercises, quizzes, and exams.

**\* Outcome #3: Method to assess student understanding**

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Worksheets, lab practical exercises, quizzes, and exams.

**\* 4. How will you know if you were successful in your efforts to teach this outcome? Outcome #1:**

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Students can differentiate between analog and digital systems and identify advantages of digital.

**\* Outcome #2: How will you know if you were successful in your efforts to teach this outcome?**

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Students can read, express, and convert between decimal, binary, 2's complement, hex, BCD, Gray's, or octal number system.

**\* Outcome #3: How will you know if you were successful in your efforts to teach this outcome?**

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Given known logical inputs students can identify the logical output of basic logic gates in a circuit.

**5. Instructor Questions: Create two course specific questions to be included on the Student Course Evaluation. Question #1**

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(No response)

**Question #2**

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(No response)

**Do you require the names of students who complete the course evaluation survey? (Please note: names will be sent to instructors the Thursday before term ends)**

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NO

**Reminder, when completing Part B, instructors will be asked the following questions: Describe anything you did to assist the institutional effort to support students in improving achievement of the specified criteria for the following Institutional Learning Outcomes (ILO): 1. ILO#1 - Communication - "Content Development" and/or "Control of Syntax and Mechanics" 2. ILO#2 - Critical Thinking/Problem Solving - "Evidence" and/or "identify strategies" 3. ILO#4 - Cultural Awareness - "Openness" (Encouraging our students to "Initiate and develop interactions with culturally different others") 4. ILO#5 - Community and Environmental Responsibility 5. ILO#3 - Quantitative Literacy - "Application/Analysis" and/or "Assumptions"**

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(No response)